

FIG. 1

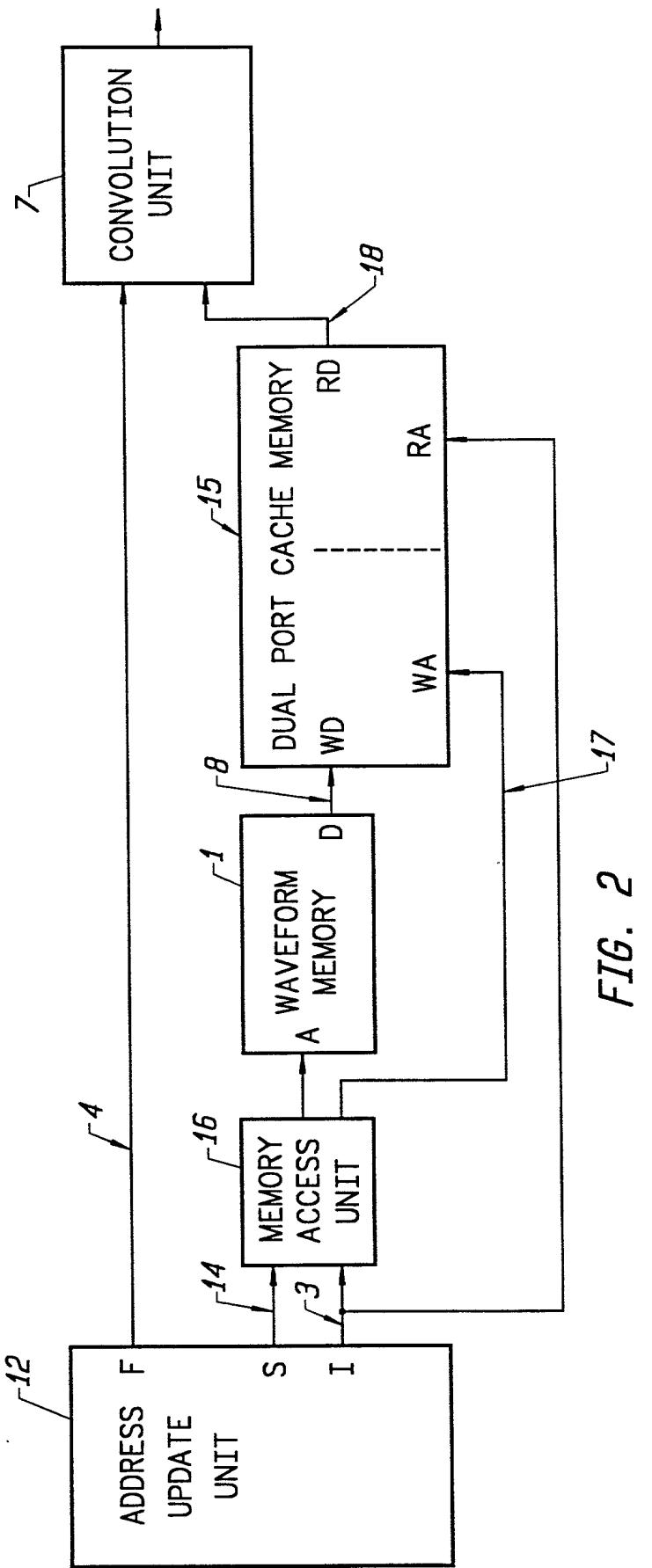


FIG. 2

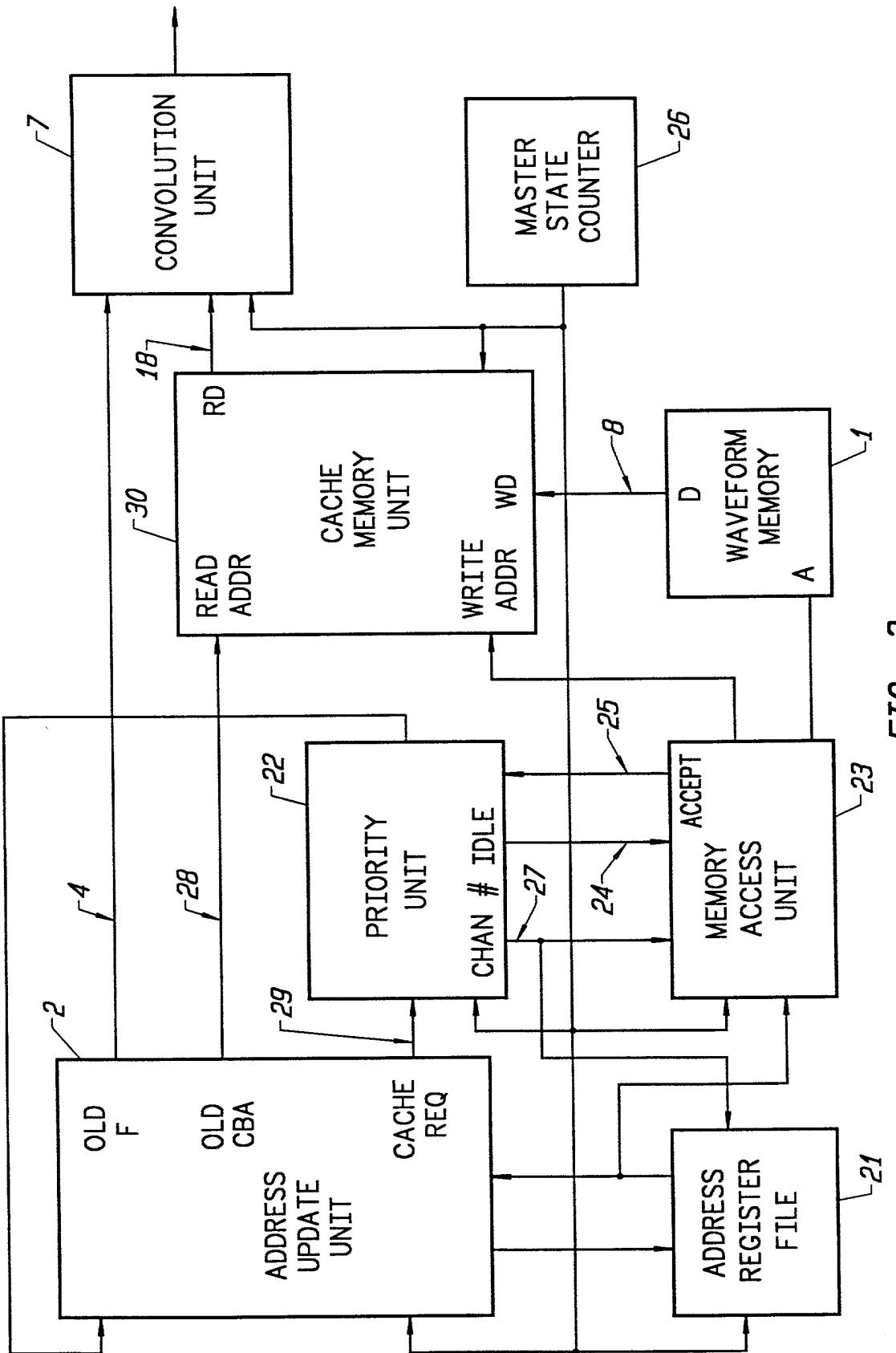


FIG. 3

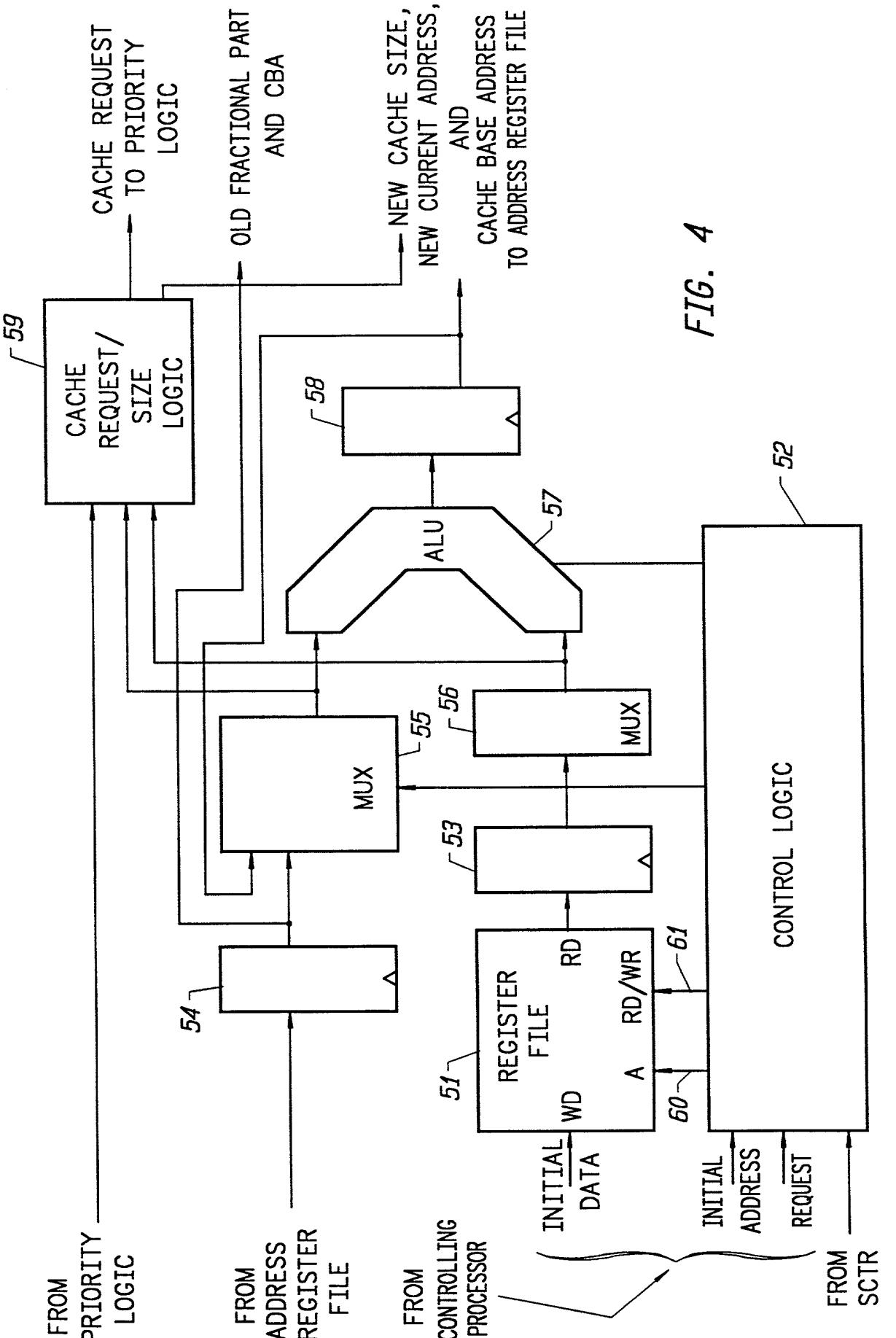


FIG. 4

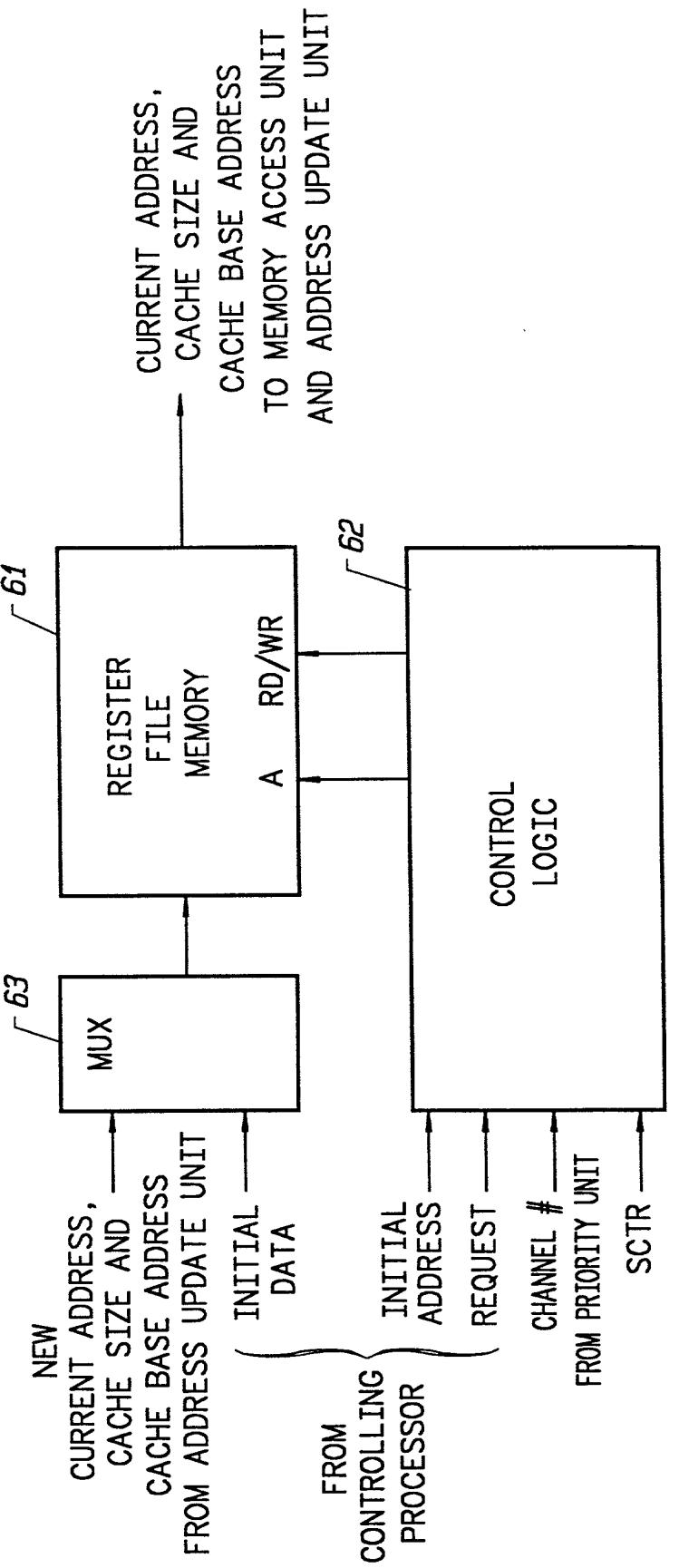


FIG. 5

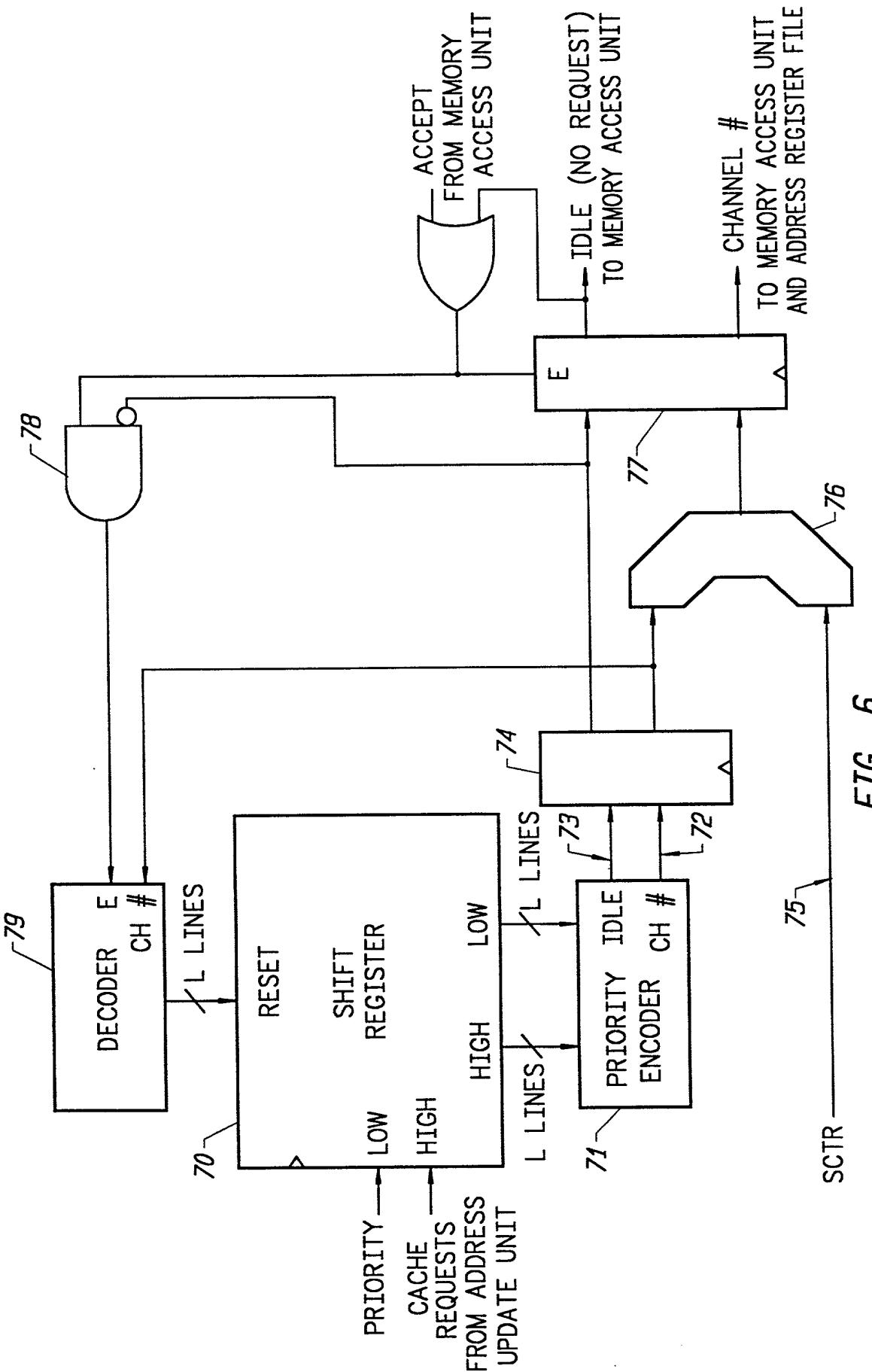


FIG. 6

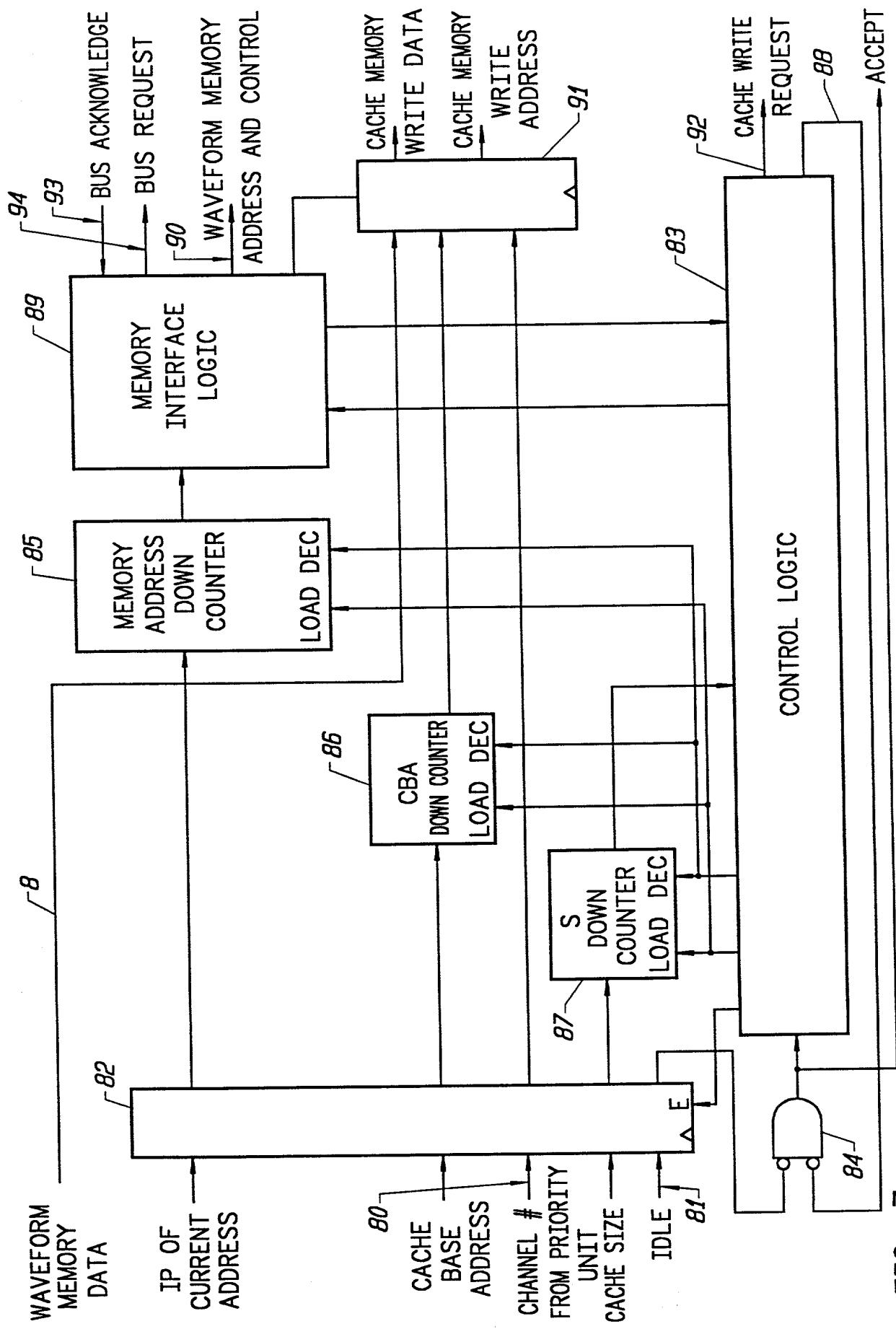
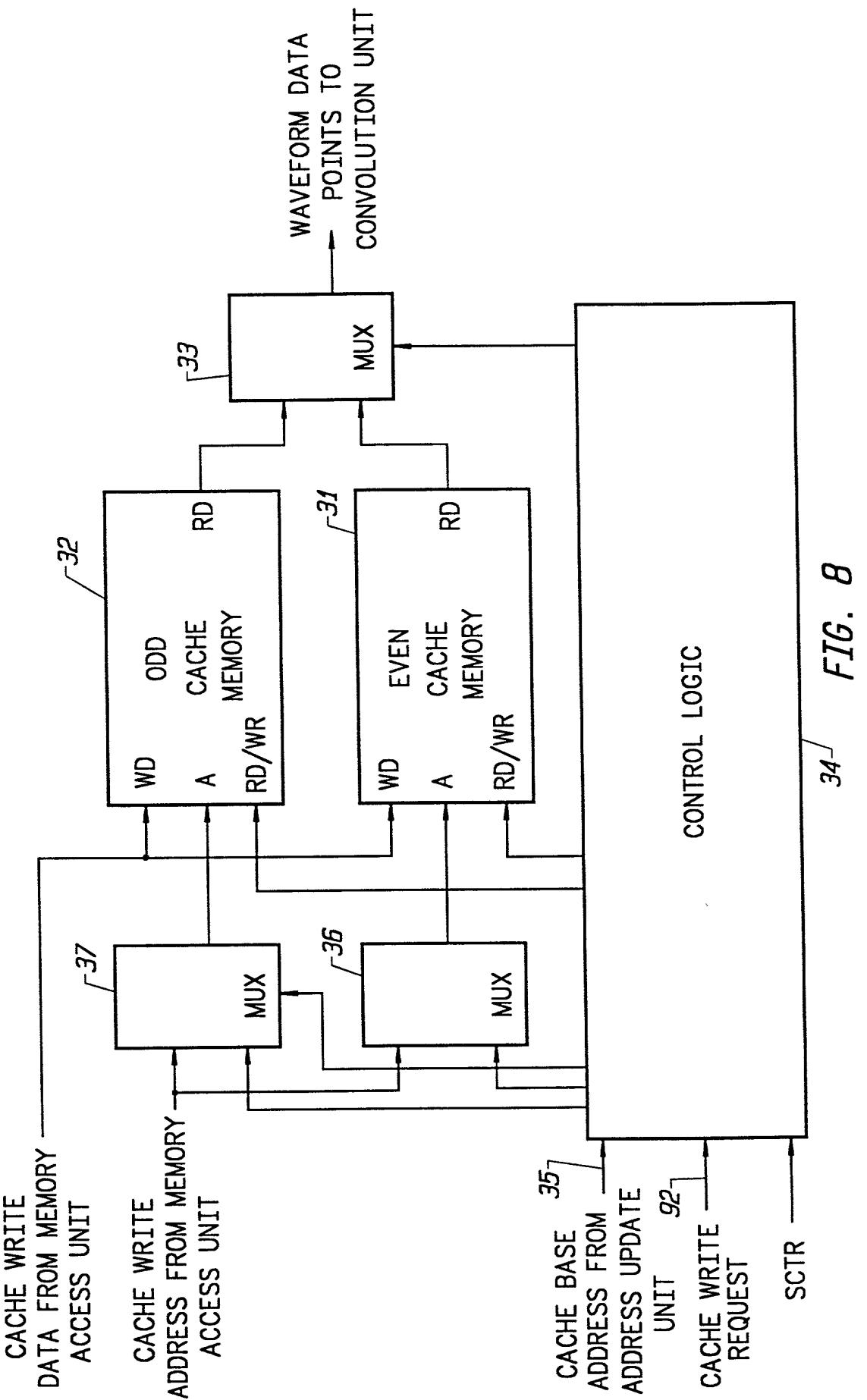


FIG. 7



34 / FIG. 8

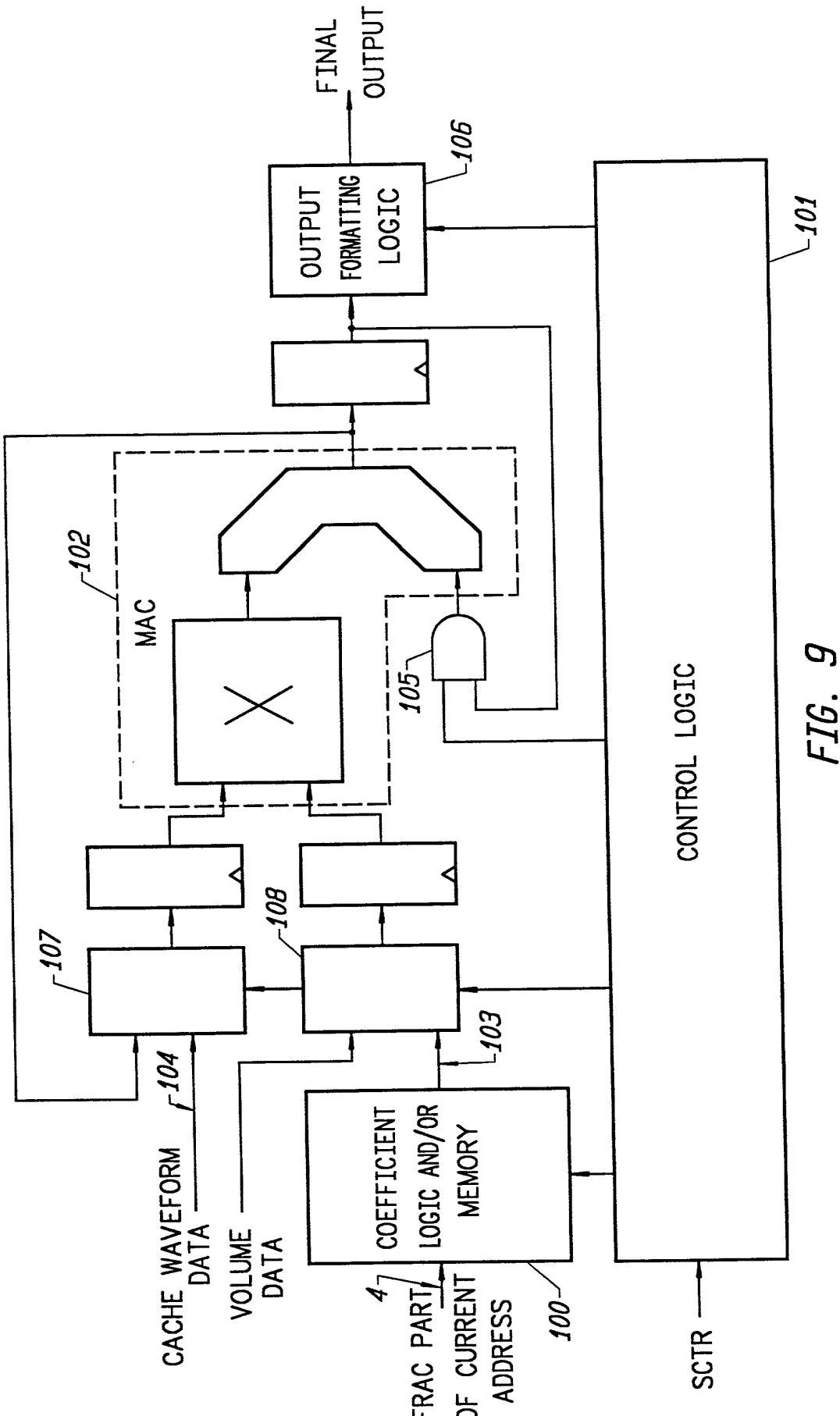


FIG. 9

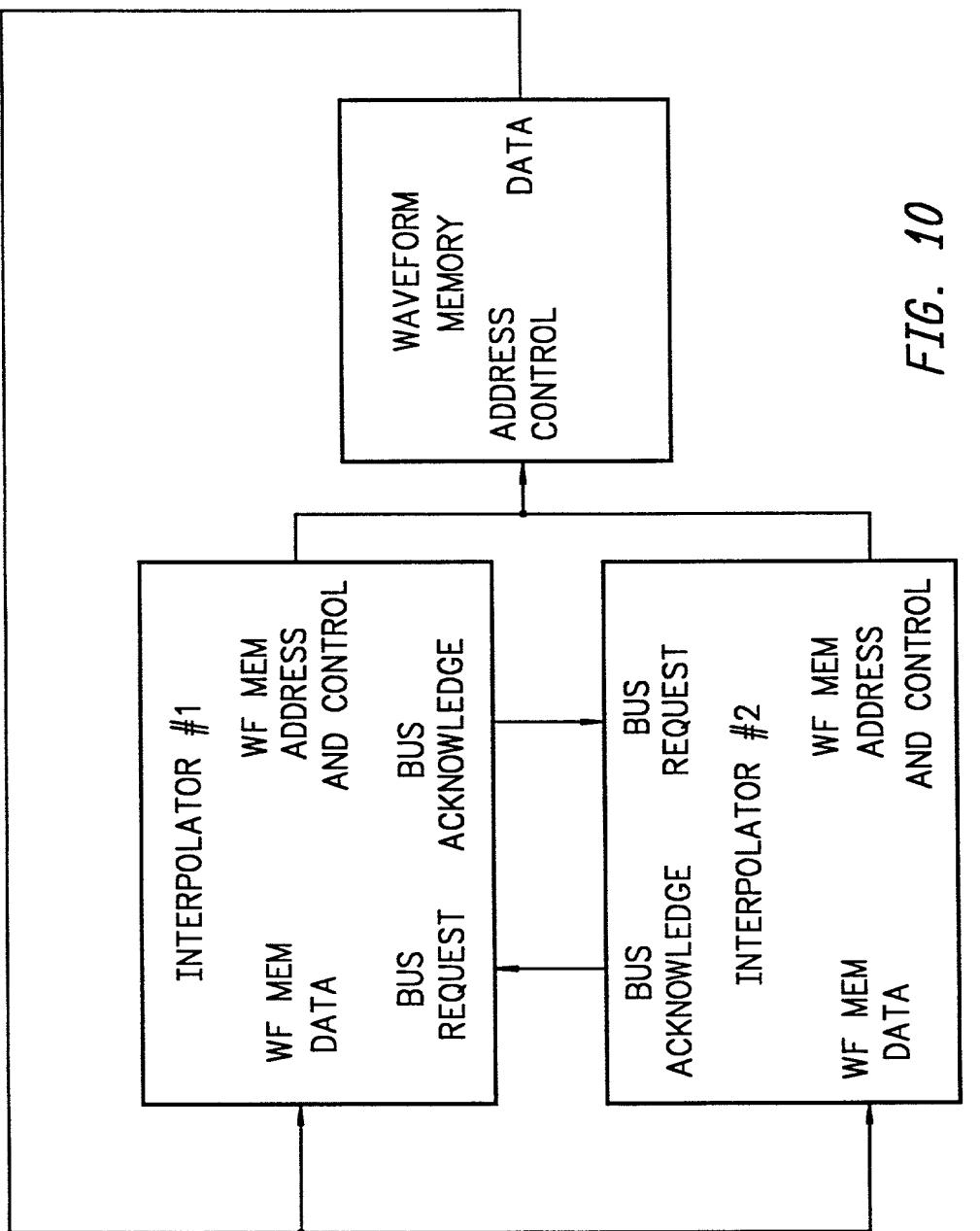


FIG. 10

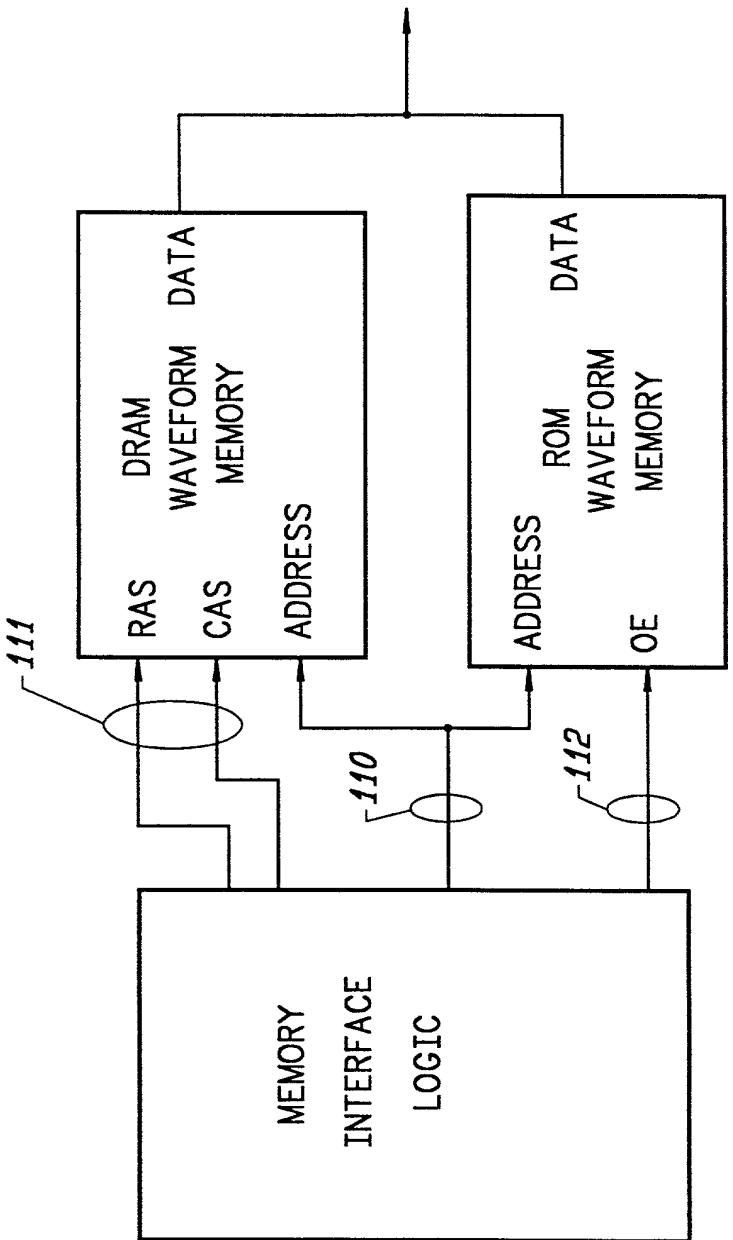


FIG. 11

STATE COUNTER (8 PER CHANNEL)	0	1	2	3	4	5	6	7	0(NEXT CHANNEL)
CBA ADDRESS	NEW VALID CACHE BASE ADDRESS ----->								DON'T CARE
CACHE READ OPERATION	NOP	READ POINT 0	READ POINT 1	READ POINT 2	READ POINT 3	READ POINT 4	READ POINT 5	READ POINT 6	NOP
CR READ OPERATION CBA EVEN	NOP	READ EVEN POINT	READ ODD POINT	READ EVEN POINT	READ ODD POINT	READ EVEN POINT	READ ODD POINT	READ EVEN POINT	NOP
CR READ OPERATION CBA ODD	NOP	READ ODD POINT	READ EVEN POINT	READ ODD POINT	READ EVEN POINT	READ ODD POINT	READ EVEN POINT	READ ODD POINT	NOP
EVEN CR OPERATION CBA EVEN	AVAIL FOR WR	READ POINT 0	AVAIL FOR WR	READ POINT 2	AVAIL FOR WR	READ POINT 4	AVAIL FOR WR	READ POINT 6	AVAIL FOR WR
ODD CR OPERATION CBA EVEN	AVAIL FOR WR	AVAIL FOR WR	READ POINT 1	AVAIL FOR WR	READ POINT 3	AVAIL FOR WR	READ POINT 5	AVAIL FOR WR	AVAIL FOR WR
EVEN CR OPERATION CBA ODD	AVAIL FOR WR	AVAIL FOR WR	READ POINT 1	AVAIL FOR WR	READ POINT 3	AVAIL FOR WR	READ POINT 5	AVAIL FOR WR	AVAIL FOR WR
ODD CR OPERATION CBA ODD	AVAIL FOR WR	READ POINT 0	AVAIL FOR WR	READ POINT 2	AVAIL FOR WR	READ POINT 4	AVAIL FOR WR	READ POINT 6	AVAIL FOR WR
MAU EVEN WRITE CBA EVEN	WRITE	WAIT	WRITE	WAIT	WRITE	WAIT	WRITE	WAIT	WRITE
MAU ODD WRITE CBA EVEN	WRITE	WRITE	WAIT	WRITE	WAIT	WRITE	WAIT	WRITE	WRITE
MAU EVEN WRITE CBA ODD	WRITE	WRITE	WAIT	WRITE	WAIT	WRITE	WAIT	WRITE	WRITE
MAU ODD WRITE CBA ODD	WRITE	WAIT	WRITE	WAIT	WRITE	WAIT	WRITE	WAIT	WRITE

FIG. 12